

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

United States

4,796,206	J. A. Boscove, H. L. Kurtz, J. E. Prince, and W. P. Wiegand	Computer Assisted Vehicle Service Featuring Signature Analysis and Artificial Intelligence
4,821,082	D. J. Frank and R. F. Marks	Heterojunction Bipolar Transistor with Substantially Aligned Energy Levels
4,829,292	S. Tokuda	Method for Displaying Box Cursors
4,845,669	B. A. Chappell, Y.-G. Lien, and J. Y. Tang	Transposable Memory Architecture
4,849,978	Y. Dishon and C. J. Georgiou	Memory Unit Backup Using Checksum
4,851,671	W. D. Pohl	Oscillating Quartz Atomic Force Microscope
4,851,711	Y. H. Chan, R. H. Nijhuis, C. G. Rivadeneira, and J. R. Struk	Asymmetrical Clock Chopper Delay Circuit
4,851,767	J.-M. Halbout, M. B. Ketchen, P. A. Moskowitz, and M. R. Scheuermann	Detachable High-Speed Opto-Electronic Sampling Probe
4,851,987	M. N. Day	System for Reducing Processor Power Consumption by Stopping Processor Clock Supply If a Desired Event Does Not Occur
4,851,995	Y. Hsu and H. Li	Programmable Variable-Cycle Clock Circuit for Skew-Tolerant Array Processor Architecture
4,852,003	A. Zamora	Method for Removing Enclitic Endings from Verbs in Romance Languages
4,852,060	L. R. Rockett, Jr.	Soft Error Resistant Data Storage Cells
4,852,061	H. C. Baron, J. J. LeBlanc, T. M. Storey, and J. W. Yoder	High Density, High Performance Register File Having Improved Clocking Means
4,852,095	D. Meltzer	Error Detection Circuit
4,852,096	R. P. Brinkman	CN ² Test Pattern Generator
4,852,173	L. R. Bahl, P. F. Brown, P. V. deSouza, and R. L. Mercer	Design and Construction of a Binary-Tree System for Language Modelling
4,853,081	W. E. Mlynko	Process for Removing Contaminant
4,853,315	D. R. McKean, R. D. Miller, J. G. Walsh, and C. G. Willson	O-Quinone Diazide Sulfonic Acid Monoesters Useful as Sensitizers for Positive Resists
4,853,346	J. M. Baker, A. C. Callegari, D. L. Lacey, and Y.-C. Shih	Ohmic Contacts for Semiconductor Devices and Method for Forming Ohmic Contacts
4,853,791	J. G. Ginther, Jr.	Electromagnetic Emission Shield for Cathode Ray Tube Display
4,853,802	D. R. Kukson, C. M. Melas, and J. M. Poss	Single Stage Feedforward Equalizer
4,853,810	W. D. Pohl, J. K. Gimzewski, and P. R. Murali	Method and Apparatus for Controlling the Flying Height of the Head in a Magnetic Storage Unit

4,853,811	W. W. Brooks, Jr., C. J. Clemen, J. T. Coffey, T. P. Coffield, and D. R. Larson	Magnetic Disk Drive with Low Profile Head-Suspension System
4,853,830	W. D. Corfits, J. T. Gullicksrud, B. L. Martin, R. W. McCutcheon, J. R. Rasmussen, G. W. Westphal, and S. E. Wheeler	Three Stage Self Alignment Structure and Method
4,853,851	A. Horsch	System for Determining the Code Coverage of a Tested Program Based upon Static and Dynamic Analysis Recording
4,853,878	M. R. Brown	Computer Input by Color Coding
4,853,967	J. R. Mandeville	Method for Automatic Optical Inspection Analysis of Integrated Circuits
4,854,038	J. P. Wiley	Modularized Fabrication of High Performance Printed Circuit Boards
4,855,197	W. Zapka, J. Kempf, J. Keyser, and K. Asch	Mask for Ion, Electron or X-Ray Lithography and Method of Making It
4,855,246	C. F. Codella and S. Ogura	Fabrication of a GaAs Short Channel Lightly Doped Drain MESFET
4,855,252	S. Peterman and D. Stanasolovich	Process for Making Self-Aligned Contacts
4,855,333	W. J. Rudik, G. P. Schmitt, and J. F. Shipley	Multiple Wire Photosensitive Adhesive for Wire Embedment
4,855,681	E. H. Millham	Timing Generator for Generating a Multiplicity of Timing Signals Having Selectable Pulse Positions
4,855,858	M. Boertzel, R. Ludwig, W. Schmits, and U. Weiss	Power Regulator with Overcurrent Protection
4,855,867	C. E. Gazdik, D. G. McBride, D. P. Seraphim, and P. A. Toole	Full Panel Electronic Packaging Structure
4,855,905	S. L. Estrada and R. R. Ploger III	Multiprotocol I/O Communications Controller Unit Including Emulated I/O Controllers and Tables Translation of Common Commands and Device Addresses
4,855,907	J. T. Ferro, Jr., P. C. Jacobs, R. M. Laye, and B. D. Starr	Method for Moving VSAM Base Clusters While Maintaining Alternate Indices into the Cluster
4,855,936	C. T. Casey and S. E. Record	Full-Screen Input/Output Application Program Interface
4,855,938	J. Gonzalez-Lopez and B. C. Liang	Hidden Line Removal Method with Modified Depth Buffer
4,855,939	W. J. Fitzgerald, Jr. and R. G. Glemser II	3D Dimensioning in Computer Aided Drafting
4,855,948	M. Bouillot, J.-L. J. Calvignac, and J.-M. L. Munier	Bypass Booster Mechanisms for the Line Scanners of a Communication Controller
4,856,000	M. Bauge, G. Boudon, P. Mollier, J.-L. Peter, and Y. J. Yamour	Duplicated Circuit Arrangement for Fast Transmission and Repairability
4,857,087	I. W. Bolton, R. N. Gibbons, and G. Dixon	Disk File Employing Dual Filters
4,857,143	J. J. Glenning, C. J. Johnson, W. P. Pawlowksi, and K. G. Sakorafos	Wet Etching of Cured Polyimide
4,857,383	J. Greschner, F. W. Schwerdt, and H. J. Trumpp	Synthetic Substrate with Adhesive Metal Layer
4,857,765	J. J. Cahill, C. L. Johnson, S. D. Lewis, T. J. Mullins, and B. R. Petz	Noise Control in an Integrated Circuit Chip
4,858,114	A. W. Heath, R. Hernandez, V. M. Hoffman, R. K. Sheppard, and S. D. Stratton	Emulation System for Automatically Modifying I/O Translation Tables in Programs Designed to Run in Different Types of Computer
4,858,149	P. Quarendon	Method and System for Solid Modelling
4,858,152	M. W. Estes and H. H. Hall	Operator Access to Monitoring Applications
4,858,179	S. Ong	Apparatus for Determining the Minimum Number of Storage Elements Required to Store the State of a Circuit
4,859,253	D. A. Buchanan, A. C. Callegari, P. D. Hob, and D. L. Lacey	Method for Passivating a Compound Semiconductor Surface and Device Having Improved Semiconductor-Insulator Interface
4,858,268	C. A. Joseph and J. R. Petrozello	Method of Using Electrically Conductive Composition
4,859,815	J. W. Irwin and J. D. Wagoner	Serial Link Transparent Mode Disparity Control
4,859,856	T. R. Groves, H. C. Pfeiffer, W. Stickel, and M. A. Sturans	Telecentric Sub-Field Deflection with Vail
4,859,880	P. W. Chung and N. N. Wang	High Speed CMOS Differential Driver
4,859,911	J. R. Kinnard and D. N. Oliver	Power Supply for Electroluminescent Panel
4,860,066	P. D. Kirchner, R. F. Marks, D. Pettit, J. M. Woodall, and S. L. Wright	Semiconductor Electro-Optical Conversion
4,860,067	T. N. Jackson, A. W. Kleinsasser, and J. M. P. Woodall	Semiconductor Heterostructure Adapted for Low Temperature Operation
4,860,138	A. W. Vinal and E. A. Cunningham	Differentially Sensitive Single Track Read/Write Head Design with Improved Biasing
4,860,189	L. J. Hitchcock	Full Bridge Power Converter Circuit
4,860,193	S. R. Bentley, D. M. Fickle, and P. R. Nylander-Hill	System for Efficiently Transferring Data Between a High Speed Channel and a Low Speed I/O Device
4,860,203	C. M. Corrigan, L. W. Hunter, and J. A. Modry	Apparatus and Method for Extracting Documentation Text from a Source Code Program
4,860,248	L. Lumelsky	Pixel Slice Processor with Frame Buffers Grouped According to Pixel Bit Width

4,860,351	S. H. Weingart	Tamper-Resistant Packaging for Protection of Information Stored in Electronic Circuitry
4,861,425	S. E. Greer and R. T. Howard, Jr.	Lift-Off Process for Terminal Metals
4,861,534	R. W. Boehm, R. A. Guinn, and H. J. Koerner	Method and Apparatus for the Injection Molding of Circuit Boards
4,862,077	T. A. Horel, E. S. Hoyt, and L. A. Walls	Probe Card Apparatus and Method of Providing Same with Reconfigurable Probe Card Circuitry
4,862,113	O. R. Buhler, B. A. Simpson, and J. C. Twombly	Sinusoidal Oscillator with Instant Start-Up
4,862,154	J. Gonzalez-Lopez	Image Display Processor for Graphics Workstation
4,862,245	R. P. Pashby, D. W. Phelps, Jr., S. J. Samuelsen, and W. C. Ward	Package Semiconductor Chip
4,862,318	J. Galvagni, J. M. Oberschmidt, and J. N. Humenik	Method of Forming Thin Film Terminations of Low Inductance Ceramic Capacitors and Resultant Article
4,862,327	D. F. Ansell, J. C. Harris, S. E. Howell, M. S. Miller, and R. D. Wvsong	Adapter Card Mounting in a Low Profile Microcomputer
4,862,350	M. A. Orr and C. E. Williams	Architecture for a Distributive Microprocessing System
4,862,408	A. Zamora	Paradigm-Based Morphological Text Analysis for Natural Languages
4,862,411	Y. Dishon and M. Y. Kim	Multiple Copy Data Mechanism on Synchronous Disk Drives
4,862,451	F. H. Closs, J. R. Mueller, and P. A. Zafropulo	Method and Apparatus for Switching Information Between Channels for Synchronous Information Traffic and Asynchronous Data Packets
4,862,461	B. Blaner	Packet Switch Network Protocol
4,862,463	C.-L. Chen	Error Correcting Code for 8-Bit-Per-Chip Memory with Reduced Redundancy
4,864,458	G. J. Demorat, Jr. and W. A. Weiler, Sr.	Electrostatic Discharge Grounding Switch and Method of Operating Same
4,864,486	J. H. Spreen	Plank and Frame Transformer
4,864,492	D. A. Blakely-Fogel, G. E. Chalemin, S. P. Cummings, and C. K. Jones	System and Method for Network Configuration
4,864,516	W. D. Gaither, L. T. Giovannetti, R. J. Grafe, L. F. Hall, G. P. Meyer, and S. T. Pancoast	Method for Implementing an On-Line Presentation in an Information Processing System
4,864,539	C.-T. K. Chuang, E. Hackbarth, and D. D. Tang	Radiation Hardened Bipolar Static RAM Cell
4,864,570	R. J. Savaglio and P. A. Twombly	Processing Pulse Control Circuit for Use in Device Performing Signature Analysis of Digital Circuits
4,866,637	J. Gonzalez-Lopez, B. C. Hempel, and B. C. Liang	Pipelined Lighting Model Processing System for a Graphics Workstation's Shading Function
4,864,722	D. J. Lazzarini, J. P. Wiley, and R. T. Wiley	Low Dielectric Printed Circuit Boards
4,865,423	A. Doi	Method for Generating Images
4,866,308	D. R. Cecchi, H. S. Kim, J. S. Mitby, D. P. Swart, B. R. Stanisic, and P. T. Wu	CMOS to GPI Interface Circuit
4,866,491	P. M. Solomon and T. N. Theis	Heterojunction Field Effect Transistor Having Gate Threshold Voltage Capability
4,866,507	S. L. Jacobs, P. Nihal, B. Ozmat, H. D. Schnurmann, and A. R. Zingher	Module for Packaging Semiconductor Integrated Circuit Chips on a Base Substrate
4,866,609	S. A. Calta, T. A. Gregg, L. Skarshinski, and R. Strangways	Byte Count Handling in Serial Channel Extender with Buffering for Data Pre-Fetch
4,866,611	C. M. N. Cree, G. J. Landry, K. J. Scully, and H. S. Singh	Method for Automatically Reconciling Entries on Two Copies of Independently Maintained Electronic Calendars
4,866,628	B. Natarajan	Automated Production Dispatch System with Feedback Control
4,866,651	J. H. Bleher, A. T. Gerlicher, S. M. Rump, and D. K. Unkauf	Method and Circuit Arrangement for Adding Floating Point Numbers
4,866,694	H. E. Korth	Read/Write Head for Optical Disks
4,866,769	A. H. Karp	Hardware Assist for Protecting PC Software
4,866,785	K. Shibano	Multi-Valved Image Processing Apparatus and Method
4,867,059	R. A. Halbeisen, S. S. Harding, and R. A. Sebrosky	Impact Printer Print Mechanism and Method of Manufacture
4,867,838	G. A. Brooks and N. A. Greco	Planarization Through Silylation
4,868,135	S. Ogura and N. Rovedo	Method for Manufacturing a Bi-CMOS Device
4,868,253	S. A. Hodgson, J. M. Park, and R. R. Thomas	β -Diketone Polymer Reaction Product
4,868,350	J. G. Hoffarth and J. P. Wiley	High Performance Circuit Boards
4,868,413	S. F. Oakland and C. R. Ogilvie	Testable Passgate Logic Circuits
4,868,492	J. G. Beha, A. U. Blacha, R. Clauberg, and H. K. Seitz	Method for Contactless Testing of Conducting Paths in a Substrate Using Photo-Assisted Tunneling
4,868,506	T. H. DiStefano, A. E. Falls, A. Halperin, and J. D. Mackay	Defect Detection Using Intermodulation Signals

4,868,514	M. J. Azevedo, C. A. Corchero, D. J. Lang, and G. R. Woodman, Jr.	Apparatus and Method for Digital Compensation of Oscillator Drift
4,868,549	F. J. Affinito and J. F. Beetem	Feedback Mouse
4,868,554	T. Aoki, K. Nojima, and Y. Seki	Display Apparatus
4,868,687	S. C. Penn and M. D. Wilkes	Audio Editor Display Interface
4,868,696	R. Hammer and S. G. Hedge	Linear Actuator Using a Compound Parallel Bendable Element Suspension System
4,868,732	J. B. Gillett and J. H. Spreen	Pluggable Power System Having Magnetic Flux Coupled Power Transformer and Inductive Filter Components
4,868,739	C. H. Ngai and G. J. Watkins	Fixed Clock Rate Vector Processor Having Exclusive Time Cycle Control Programmable into Each Microword
4,868,744	R. A. Reinsch and M. R. Zimowski	Method for Restarting a Long-Running, Fault-Tolerant Operation in a Transaction-Oriented Data Base System Without Burdening the System Log
4,868,782	S. G. C. Lawrence, M. A. McMorran, and B. H. Middleton	Display Terminal
4,868,783	G. D. Anderson, G. J. Hladik, L. G. Mosher, R. L. Ricci, and H. Yeh	Dyanmic Port Reconfiguration
4,868,863	J. D. Hartley, M. Germain, B. D. Key, R. Jepson, V. G. Golding, S. Cook, and P. D. Maddison	Apparatus for Connecting Data Processing Equipment to a Communication Network
4,869,418	J. P. Simpson, G. L. Newman, J. M. Larned, and A. L. Emerick	Solder Leveling Method and Apparatus
4,869,777	M. Apschel, F. D. Egitto, R. S. Horwath, and G. Koren	Method for Selectively Etching the Materials of a Composite of Two Materials
4,869,781	W. Euen, D. Hagmann, and H.-J. Trumpp	Method for Fabricating a Semiconductor Integrated Circuit Structure Having a Submicrometer Length Device Element
4,869,876	K. D. Arfman, T. J. Pillera II, R. R. Ruckel, A. C. Turits, and J. Q. Walker	Multi-Purpose Plural-Oven Gas Chromatography System with Shared Controls
4,869,930	T. C. Clarke, C. A. Kovac, D. Y. Jung, J. M. Park, and R. R. Thomas	Method for Preparing Substrates for Deposition of Metal Seed from an Organometallic Vapor for Subsequent Electroless Metallization
4,870,052	E. M. Engler, V. Y. Lee, A. I. Nazzal, and S. P. Parkin	Ti-Ca-Ba-Cu-O Compositions Electrically Superconducting Above 120 Degree K and Processes for Their Preparation
4,870,272	J. T. Wilson III	Transducer Adjustment Apparatus for Shaft Encoder
4,870,406	S. Gupta, L. Lumelsky, R. L. Mansfield, H. G. Romero, Jr., M. Segre, A. K. Spencer, J. C. St. Clair, and J. D. Wagoner	High Resolution Graphics Display Adapter
4,870,414	R. Karabed and P. H. Siegel	Even Mark Modulation Coding Method
4,870,433	A. S. Campbell, J. M. Eldridge, F. C. Lee, and G. Olive	Thermal Drop-on-Demand Ink Jet Print Head
4,870,470	R. S. Bass, Jr., A. Bhattacharyya, and G. D. Grise	Non-Volatile Memory Cell Having Si Rich Silicon Nitride Charge Trapping Layer
4,870,566	R. J. Cooper, M. A. Marsico, R. C. Matlack, J. C. Pescatore, and R. L. Smith, Jr.	Scannerless Message Concentrator and Communications Multiplexer
4,870,591	R. J. Ciciarelli and D. B. Millis	System for Ensuring Device Compatibility
4,870,599	B. C. Hempel, G. D. Laib, and B. C. Liang	Traversal Method for a Graphics Display System
4,870,611	M. S. Martin, H. D. Puckett, Jr., and T. W. Srutchin, Jr.	Apparatus and Method for System Printing Mode Control
4,870,635	T. R. Block, M. R. Enstrom, F. S. Luecke, K. A. Shidler, and R. L. Soderstrom	Precision Measurement and Positioning System for Disk Storage System
4,870,695	C. A. Gonzales, J. L. Mitchell, and W. B. Pennebaker, Jr.	Compression and De-Compression of Column-Interlaced, Row-Interlaced Graylevel Digital Images
4,871,418	J. Wittlinger, J. Greschner, T. Bayer, and J. W. Bartha	Process for Fabricating Arbitrarily Shaped Through Holes in a Component
4,871,619	C. J. Araps, S. M. Kandetzke, and M. A. Takacs	Electronic Components Comprising Polyimide Dielectric Layers
4,871,630	N. J. Giammarco, A. Gimpelson, G. A. Kaplita, A. D. Lopata, A. F. Scaduto, and J. F. Shepard	Mask Using Lithographic Image Size Reduction
4,873,634	R. A. Frisch and A. Zamora	Spelling Assistance Method for Compound Words
4,871,684	R. Glang and S.-M. Ku	Self-Aligned Polysilicon Emitter and Contact Structure for High Performance Bipolar Transistors
4,871,919	T. P. Donohue, F. J. Hohn, and G. A. Sai-Halasz	Electron Beam Lithography Alignment Using Electric Field Changes to Achieve Registration
4,871,974	G. T. Davis and B. D. Mandalia	Coherent Phase Shift Keyed Demodulator
4,872,040	T. N. Jackson	Self-Aligned Heterojunction Transistor
4,872,071	M. C. Easton and P. B. P. Phipps	Method and Apparatus for Detecting Abnormal Operation of Moving Storage Apparatus
4,872,078	U. G. Gerber and D. Rugar	Method and Apparatus for Encoding and Direct Overwriting of Magneto-Optic Data

4,872,107	M. J. Marple and A. S. Potemski	Floppy Disk Controller with Means to Change Clock Rate Automatically
4,872,381	K. F. Stroms	Programmable Magnetic Repulsion Punching Apparatus
4,872,661	J. A. Knepper	Roll Release Mechanism
4,872,851	R. Babuka and J. R. Petrozello	Electrical Connector with Torsional Contacts
4,873,123	M. J. Canestaro and W. J. Summa	Flexible Electrical Connection and Method of Making Same
4,873,430	A. Juliana, W. C. Leung, V. T. Pan, and H. J. Rosen	Method and Apparatus for Optically Measuring Characteristics of a Thin Film by Directing a P-Polarized Beam Through an Integrating Sphere at the Brewster's Angle of the Film
4,873,514	B. Nakagawa and K. Nojima	Video Display System for Scrolling Text in Selected Portions of a Display
4,873,517	A. E. Baratz, K. E. Clarke, M. R. Pollard, D. P. Pozefsky, L. M. Rafalow, W. E. Siddall, and J. P. Gray	Method for Selecting Least Weight End Node to End Node Route in a Data Communications Network
4,873,599	K. Sueoka	Magnetic Head Assembly for Perpendicular Magnetic Recording
4,873,625	G. D. Archer, E. G. Huff, M. T. Madrid, Jr., and A. Yoshii	Method and Apparatus for Extending Collation Functions of a Sorting Program
4,873,687	W. W. Breu	Failing Resource Manager in a Multiplex Communication System
4,874,682	J. C. Scott and M. D. Shattuck	Organic Photoconductors with Reduced Fatigue
4,874,931	Y. Oka and K. Sato	Method for Automatic Bill Handling and Bill Container Used Therefor
4,874,998	R. L. Hollis, Jr.	Magnetically Levitated Fine Motion Robot Wrist with Programmable Compliance
4,875,112	M. H. Dost, E. Hopner, C. M. Melas, and L. D. Provazek	Compound Pulse Dimming Circuitry for Conditioning Readback Signals
4,875,124	H. L. Dickstein, H. Hiraoka, and J. H. Lee	Thin Film Magnetic Heads with Thermally Crosslinked Insulation
4,875,155	J. L. Iskiyan, V. J. Legvold, P. L. Leung, and G. E. Martin	Peripheral Subsystem Having Read/Write Cache with Record Access
4,875,162	K. M. Ferriter and R. B. Mathis	Automated Interfacing of Design/Engineering Software with Project Management Software
4,875,614	T. M. Cipolla, R. R. Horton, P. Murphy, and M. J. Palmer	Alignment Device
4,876,114	R. R. Phinney	Process for the Self Fractionation Deposition of a Metallic Layer on a Workpiece
4,876,607	S. C. Tseng	Complex Character Generator Utilizing Byte Scanning
4,876,644	D. W. Nuechterlein and M. A. Rinaldi	Parallel Pipelined Processor
4,876,720	T. Kaneko and O. Watanuki	Speech Recognition System
4,876,733	M. A. Lavin	Method for Performing Morphic Transformations on Image Data in a General Purpose Computer
4,877,204	J. A. Gregory and D. L. Schaum	Tilt/Swivel Display Base
4,877,718	J. A. Moore, A. N. Dasheff, and F. B. Kaufman	Positive-Working Photosensitive Polyimide Operated by Photo Induced Molecular Weight Changes
4,878,108	D. W. Phelps, Jr. and W. C. Ward	Heat Dissipation Package for Integrated Circuits
4,878,167	K. M. Kapulka, H. A. Rader, and J. P. Strickland	Method for Managing Reuse of Hard Log Space by Mapping Log Data During State Changes and Discarding the Log Data
4,878,182	M. A. Aranda, T. J. Ebbes, Y. Iida, T. W. Lindgren, and T. H. Robertson	Multiple Pixel Generator
4,878,209	R. W. Bassett, W. R. Griffin, S. A. Murphy, J. G. Petrovik, Jr., J. R. Varner, and D. R. Whittaker	Macro Performance Test
4,878,290	C. R. Masud and C.-T. Wu	Method for Making Thin Film Magnetic Head
4,879,433	J. B. Gillett, F. H. Lohrey, and J. A. Lorenzen	Parallel Cable Bus With Internal Path Length Compensation
4,879,551	C. J. Georgiou, Y.-C. L. Lien, and K. Maruyama	Switching Array with Concurrent Marking Capability
4,879,577	S. A. Mabrouk and G. L. Wheeler	Method and Apparatus for Controlling the Electrostatic Parameters of an Electrophotographic Reproduction Device
4,879,610	S. A. Jove, K. B. Klaassen, C. S. Nomura, and J. C. L. vanPeppen	Protective Circuit for a Magnetoresistive Element
4,879,619	R. E. Fontana, Jr., J. K. Howard, J. H. Lee, and H. Lefakis	Magnetoresistive Read Transducer
4,879,661	F. W. Olsen	Bi-Directional Circuit to Interface Between a Low Current Device and High Current Tester
4,879,675	T. B. Brodnax	Parity Generator Circuit and Method
4,879,745	Y. Arbel	Half-Duplex Speakerphone
4,880,684	D. W. Boss, T. W. Carr, D. J. Dubetsky, G. M. Greenstein, W. D. Grobman, C. P. Hayunga, A. H. Kumar, W. F. Lange, R. H. Massey, P. H. Palmateer, J. A. Romano, and D.-Y. Shih	Sealing and Stress Relief Layers and Use Thereof
4,880,722	W. M. Moreau and K. N. Chiong	Diazoquinone Sensitized Polyamic Acid Based Photoresist Composition Having Reduced Dissolution Rates in Alkaline Developers

4,880,754	A. Correale	Method for Providing Engineering Changes to LSI PLAS
4,880,959	T. H. Baum, P. B. Comita, and R. L. Jackson	Process for Interconnecting Thin-Film Electrical Circuits
4,881,076	J. J. Ashley, M. G. Call, and P. H. Siegel	Encoding for Pit-Per-Transition Optical Data Recording
4,881,105	B. Davari, W. Hwang, and N. C. Lu	Integrated Trench-Transistor Structure and Fabrication Process
4,881,164	B. T. Hailpern, L. W. Hoevel, and E. Shapiro	Multi-Microprocessor for Controlling Shared Memory
4,881,179	J. P. Vincent	Method for Providing Information Security Protocols to an Electronic Calendar
4,881,230	M. E. Clark, R. G. Greever, L. J. Schmier, and J. D. Wong	Expert System for Processing Errors in a Multiplex Communications System
4,881,885	C. A. Kovac, P. G. Ledermann, and L. T. Nguyen	DAM for Lead Encapsulation
4,882,245	J. D. Gelorme, R. J. Cox, and S. A. R. Gutierrez	Photoresist Composition and Printed Circuit Boards and Packages Made Therewith
4,882,505	A. Furman	Fully Synchronous Half-Frequency Clock Generator
4,882,583	K. E. Dimitri, M. A. Hassner, and P. H. Siegel	Modified Sliding Block Code for Limiting Error Propagation
4,882,608	T. P. Smith III	Multilayer Semiconductor Device Having Multiple Paths of Current Flow
4,882,642	G. E. Tayler and R. E. Wagner	Sequentially Processing Data in a Cached Data Storage System
4,882,691	M. I. Schor	Caching Argument Values in Pattern-Matching Networks
4,882,699	M. C. Evensen	Communications Network Routing and Management System
4,882,759	L. R. Bahl, P. V. deSouza, R. L. Mercer, and M. A. Picheny	Synthesizing Word Baseforms Used in Speech Recognition
4,883,744	N. B. Feilchenfeld, S. J. Fuerniss, J. J. Glenning, W. P. Pawlowski, G. M. Phelan, and P. G. Rickerl	Forming a Polyimide Pattern on a Substrate
4,884,131	D. S. Chevion, E. D. Karnin, and E. Walach	Stereoscopic Television System with Predictive Coding to Minimize Data Bandwidth
4,884,190	C. H. Ngai and G. J. Watkins	High Performance Parallel Vector Processor Having a Modified Vector Register/Element Processor Configuration
4,884,199	W. A. Boothroyd, J. C. Camann, and R. A. Palo	User Transaction Guidance
4,884,218	P. W. Agnew, N. H. Clayton, M. Judkovics, J. L. Rivero, and K.-C. Sun	Knowledge System with Improved Request Processing
4,884,237	W. R. Mueller and G. W. Spencer II	Stacked Double Density Memory Module Using Industry Standard Memory Chips
4,884,271	F. Concha and C. J. Stancil	Error Checking and Correcting for Read-Modified-Write Operations
4,884,334	D. E. Houser and R. J. Morenus	Resonant Stylus Support
4,884,857	R. Prakash and L. L. Wolfe	Scanner for Use in Multiple Spot Laser Electrophotographic Printer
4,884,905	N. C. Smith	Print Head Incorporating One Piece Armature with Intermediate Clamping Pads
4,885,038	H. R. Anderson, Jr., R. S. Divakaruni, J. M. Dynys, S. M. Kandetzke, D. P. Kirby, R. N. Master, and J. A. Casey	Method of Making Multilayered Ceramic Structure Having an Internal Distribution of Copper-Based Conductors
4,885,074	R. A. Susko and J. W. Wilson	Plasma Reactor Having Segmented Electrodes
4,885,576	W. P. Pennebaker, J. L. Mitchell, and F. C. Mintzer	Soft Copy Display of Facsimile Images
4,885,578	K. J. Perry, Y. J. Thefaine, B. T. Hailpern, L. W. Hoevel, and D. G. Shea	In Band Connection Establishment for a Multiple Multi-Drop Network
4,885,627	N. P. Edwards	Method and Structure for Reducing Resistance in Integrated Circuits
4,885,680	J. H. Anthony, W. C. Brantley, Jr., K. P. McAuliffe, V. A. Norton, and G. F. Pfister	Method and Apparatus for Efficiently Handling Temporarily Cacheable Data
4,885,684	E. B. Austin and J. E. Robertson	Method for Compiling a Master Task Definition Data Set for Defining the Logical Data Flow of a Distributed Processing Network
4,885,720	C. P. Miller and C. S. Patton	Memory Device and Method Implementing Wordline Redundancy without an Access Time Penalty
4,885,747	H. R. Foglia	Broadband and Baseband LAN
4,885,786	K. L. Anderson, W. B. Pennebaker, and K. S. Pennington	Method for Enlarging an Image Stored in Run Representation Form
4,885,789	W. F. Burger, M. E. Carson, A. Johri, and E. J. Stokes	Remote Trusted Path Mechanism for TELNET
4,886,727	M. Anschel, L. D. Goodrich, and B. M. Hetrick	Method for Developing Negative Photoresists
4,886,734	J. A. Moore, A. N. Dasheff, and F. B. Kaufmann	Electron-Beam Positive Polyimide
4,887,206	B. Natarajan	Automated System for Estimating Impact on Inventory Cost Due to an Engineering Change to a Component
4,887,207	B. Natarajan	Automated System for Evaluating the Sensitivity of Inventory Costs Due to Fluctuations in Customer Demand

4,887,212	A. Zamora, M. D. Gunther, and E. M. Zamora	Parser for Natural Language Text
4,887,218	B. Natarajan	Automated Production Release System
4,887,257	J. Belloc, E. Morlec, D. Godard, and M. Quintin	Echo Cancelling Device for Data Transmission Over Two-Wire Lines
4,887,258	J. Belloc, D. Godard, E. Morlec, and M. Quintin	Echo Cancelling Device with Phase-Roll Correction